



## **1. Scope**

This specification outlines the pertinent electrical requirements of the RF output modulator which converts the FM video and FM audio signal into the RF signal for television standard transmission system.

## **2. GENERAL SPECIFICATIONS**

2-1. Output frequency 1100~1300mhz (I<sup>2</sup>C PLL controller from outside)

2-2. Supply voltage 12V+/-0.2V

2-3. Consumption current 140+/-20mA

2-4. Operation and storage Temperature 0-50°C

Conditions for guarantee Humidity 85% or less

## **3. Test Conditions**

3-1. Testing ambient conditions

Defined as temperature of 25+/-2°C and humidity of 65+/-5% RH.

Note: That temperatures of 5-30°C and humidity of 45-85%RH may be regarded as standard.

3-2. Unit setting conditions

(1). Picture --10 step wave signal 1.0Vp-p(8Ohm load)

(2). Audio -- 1.0Vp-p of sine wave 1KHz



## 4. Electrical Performance

### 4-1. Video system characteristics

	parameter	Specification			Unit	Remark
		Min	Typ	Max		
4-1-1	Input impedance		1.7		KOhm	Measure at 0.5-5MHz
4-1-2	Input signal level		1.0		Vp-p	Load of 82Ohm connected negative synchronous
4-1-3	Modulation 1100~1300 sine wave 10khz 1Vp-p	5	6	7	MHz	Superimposed sinuous wave. (3.58MHz)is 20% of the step input
4-1-4	Differential gain	8		8	%	level measure under the apl of 10-90% differential gain of demodulator unit is to be compensated
4-1-5	Differential phase	-8		8	deg	-ditto-
4-1-6	S/N	45			dB	Measure with respect to standard demodulator output.
4-1-7	Out level taper		4	6	dB	fp 1100~1300MHz

### 4-2. Audio system characteristics

4-2-1	Input impedance		1.4		KOhm	Measure at 0.1-10KHz
4-2-2	Modulation	35	40	45	KHz	
4-2-3	Distortion factor			3	%	Audoi input signal 1.0Vp-p 1KHz modulation 50% (sine wave) video input signal all black (sync.only) use standard demodulator of inter -carrier system. De-emphasis(50 usec) is on.
4-2-4	S/N	40			dB	The same as 4-2-3



## 4-3. Output system characteristics

Parameter		Specification.				Remark
		Min	Typ	Max	Unit	
4-3-1	Video carrier frequency	50	fp	+50	KHz	Test at 25°C temperature and 65%RH of humidity Fs1 6.5 MHz Fs2 6.5 MHz *output channel
4-3-2	Video output level 1100~1300MHz	15	17	19		
4-3-3	Audio output level difference(p/s ratio) fp:1100~1300MHz	22	27	32	dB	
4-3-4	Audio carrier frequency	-8	fs <sub>1</sub>	+8	KHz	Input signal none the measurement is taken after 30 sec. from the power-on.
4-3-5	Audio modulator fs1 fs2	60	70	80	KHz	Measurement difference video of carrier frequency output level for 0-1GHz. except to fp. fp+/-fs . against video carrier output level.
4-3-6	Out-band spurious	50	55		dB	
4-3-7	Output impedance		75		Ohm	Unbalanced.



## 5-1. PLL section characteristics

No	Item	Specification								notes																																																																																					
5-2.	IIC Bus (1) Sda.scl Input voltage	Under standard test condition																																																																																													
		<table border="1"> <thead> <tr> <th>Condition</th><th>Min</th><th>Typ</th><th>Max</th></tr> </thead> <tbody> <tr> <td>High voltage</td><td>3</td><td></td><td>5</td></tr> <tr> <td>Low voltage</td><td>0</td><td></td><td>1.5</td></tr> </tbody> </table>								Condition	Min	Typ	Max	High voltage	3		5	Low voltage	0		1.5																																																																										
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	(2) Address	C2 (on write date format)																																																																																													
	(3) Sda scl Input impedance	SDA/SCLI are in the high impedance and there should be no reliability problem with 5V continually on the SDA/SCL,if power supply is switched off.								V																																																																																					
	(4) Data format	<table border="1"> <thead> <tr> <th colspan="4">MSB</th><th colspan="4">LSB</th></tr> <tr> <th>Address</th><th>1</th><th>1</th><th>0</th><th>0</th><th>0</th><th>MA1</th><th>MA0</th><th>0</th><th>A</th><th>Byte1</th></tr> </thead> <tbody> <tr> <td>Programmable Divider</td><td>0</td><td>14</td><td>13</td><td>12</td><td>11</td><td>10</td><td>9</td><td>8</td><td>A</td><td>Byte2</td></tr> <tr> <td>Programmable Divider</td><td>2</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td><td></td><td></td></tr> <tr> <td>Charge pump and test bits</td><td>2</td><td>2</td><td>2</td><td>2</td><td>2</td><td>2</td><td>2</td><td>2</td><td>A</td><td>Byte3</td></tr> <tr> <td>I/O port control bits</td><td>1</td><td>(0)</td><td>t1</td><td>t0</td><td>1</td><td>1</td><td>1</td><td>(0)</td><td></td><td></td></tr> <tr> <td></td><td>p7</td><td>p6</td><td>p5</td><td>p4</td><td>p3</td><td>p2</td><td>p1</td><td>p0</td><td>A</td><td>Byte4</td></tr> <tr> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>Byte5</td></tr> </tbody> </table>									MSB				LSB				Address	1	1	0	0	0	MA1	MA0	0	A	Byte1	Programmable Divider	0	14	13	12	11	10	9	8	A	Byte2	Programmable Divider	2	6	5	4	3	2	1	0			Charge pump and test bits	2	2	2	2	2	2	2	2	A	Byte3	I/O port control bits	1	(0)	t1	t0	1	1	1	(0)				p7	p6	p5	p4	p3	p2	p1	p0	A	Byte4											Byte5
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	A: Acknowledge bit.																																																																																														
	MA1,MA0: Voltage address bits.																																																																																														
	CP: Charge pump current select.																																																																																														
	T1: Test mode selection,T0:Charge pump disable																																																																																														
	OS: Varactor drive output disable switch.																																																																																														
	P7,P6,P5,P4,P3,P2,P1,P0: Control output states.																																																																																														
	POR: Power on reset indicator																																																																																														
	PL: Phase lock detect flag																																																																																														
	I2,I1,I0: Digital information from ports P7,P5, and P4.																																																																																														
	A2,A1,A0: 5 level adc data from P6																																																																																														

